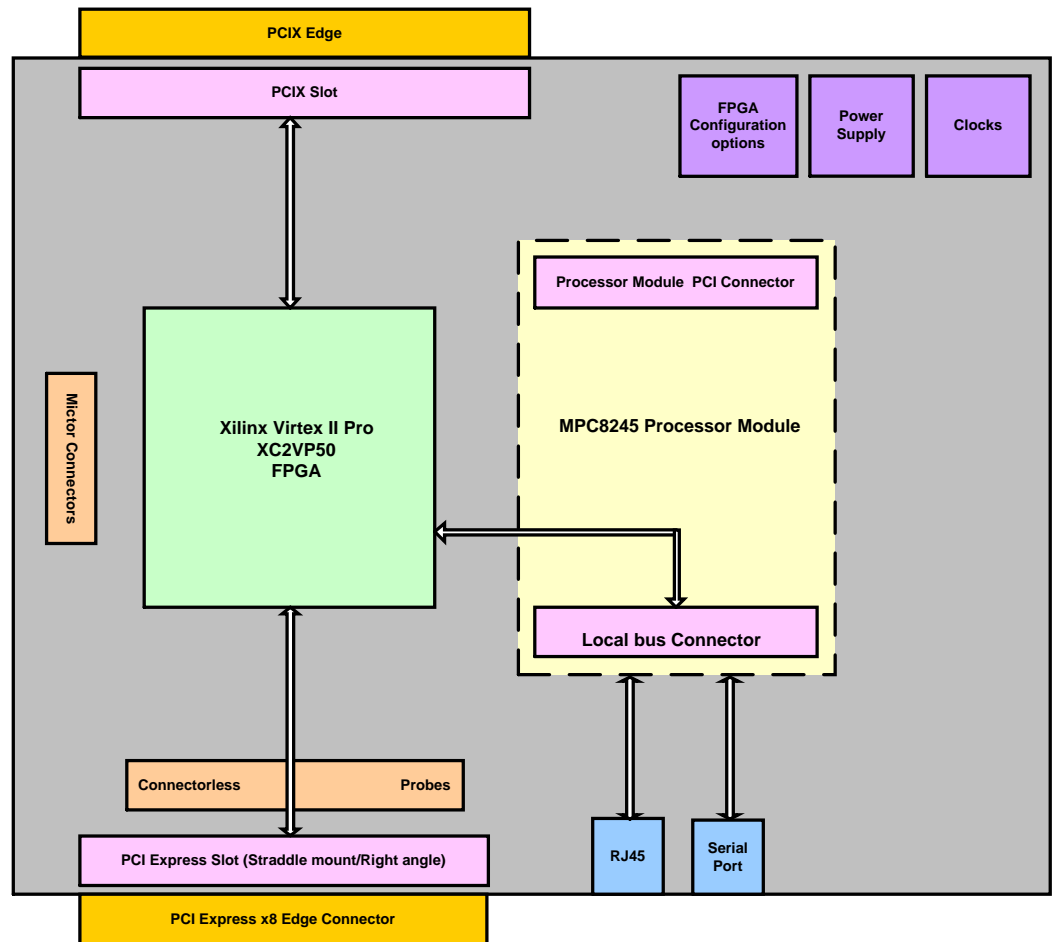




GDA Technologies' GPEX-3000 Board Intellectual Property Core Evaluation Platform

Evaluation
Boards



Overview

GDA Technologies' GPEX-3000 board is an Intellectual Property evaluation platform intended for validating a variety of RTL cores. The GPEX-3000 architecture is optimized for high-density and high-performance logic designs. The rich feature set, based on today's high speed networking technology, allows functional testing of various types of Intellectual Property cores by using Xilinx Virtex II Pro XC2VP50 FPGA. By using the high-density FPGA, the GPEX-3000 can prototype an ASIC equivalent of approximately 600K gates.

Different high speed interconnects like PCI Express, PCIX are supported in this platform.



Evaluation Boards

The board provides for easy configuration of the FPGA either through a JTAG connector with a multiLINX cable or with a standard Flash device through an on-board CPLD.

The management portion includes a CPU subsystem, which can be plugged in as a module. CPU module with MPC8245 processor from Motorola supported by 8MB Flash Memory and 128MB SDRAM. There is a serial port and an Ethernet management port available on-board to facilitate debug activity.

The board can operate as a standalone hardware module or as a PCI/PCI-X or PCIe add on card, thereby enhancing the application of the board.

There is an ATX power input connector to support GPEX-3000 in standalone mode. Alternately, power can be drawn from the PCI/PCIe interface connector if the board is operating in target mode.

Features

- Xilinx Virtex II Pro XC2VP50 FPGA
- x8 PCI Express connectivity
- 64 bit PCIX Connectivity
- Mictor connectors for debug signals
- Motorola MPC8245 based module
 - Serial Port
 - 10/100 Ethernet Port
 - 8MB Flash Memory
 - 128 MB SDRAM
 - Linux Operating System

Cost

The GPEX-3000 is priced at \$7,500 in quantities less than 2. The minimum lot order is two pieces.