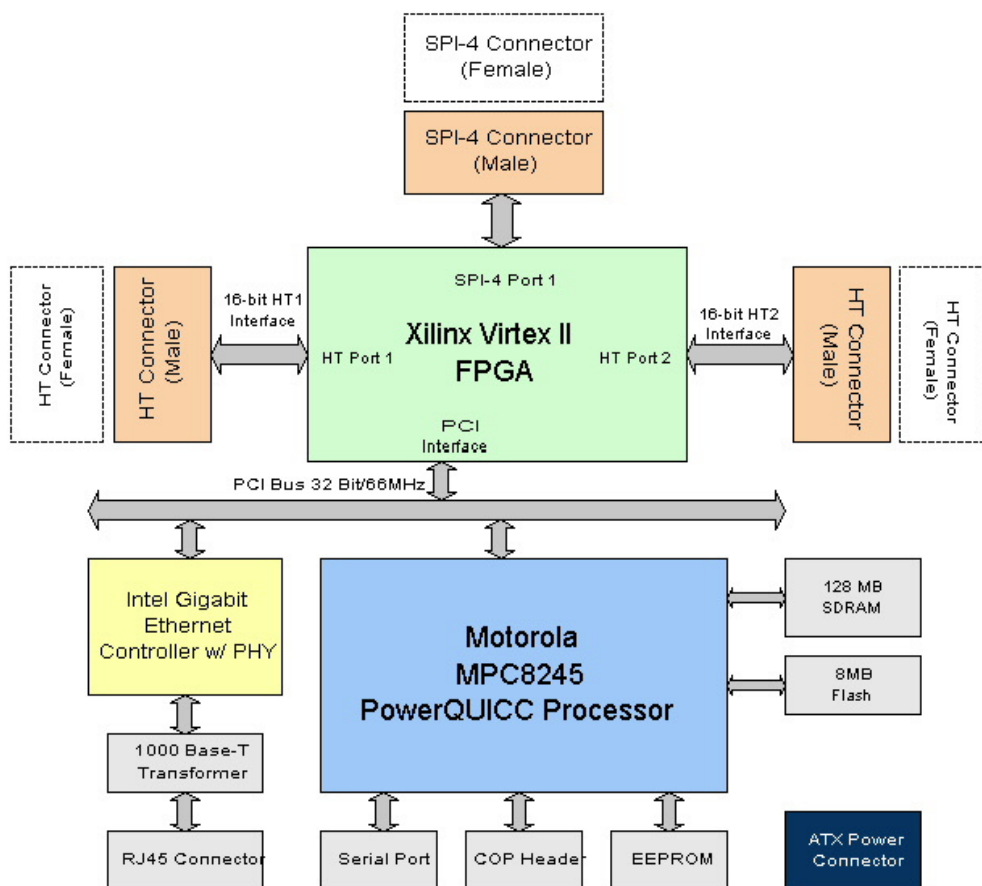


GDA Technologies' HyperTransport™ Validation Platform

Evaluation Boards



Overview

GDA Technologies' HT8000 HyperTransport evaluation platform is a high density, high performance, FPGA based test platform designed to validate HyperTransport cores. This rich feature set platform has a Virtex II FPGA from Xilinx and provides two 16-bit HyperTransport Interfaces to allow testing of host, tunnel or cave based HyperTransport configurations. The platform also provides a 16-bit SPI4 and Gigabit Ethernet interface extending its use to validate cores that bridge HyperTransport, SPI-4 and Gigabit Ethernet technologies.

The HT8000 provides for easy configuration of the FPGA either through a JTAG connector with a multiLINX cable and Xilinx programming software or through an EEPROM.

The management portion includes a CPU subsystem using MPC8245 PowerQUICC II processor from Motorola supported by 8MB Flash Memory and 128 MB SDRAM. There is a serial port available on-board to facilitate debug activity.

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The platform is equipped with high speed male and female connectors from Samtec (QSE and QTE connectors), that are compliant with the HyperTransport Consortium connector definition. This design allows the user to stack multiple HT8000 units or other compatible devices thereby facilitating a variety of test configurations. On board debug headers provide connection to HyperTransport Analyzer from FuturePlus.

There is an ATX power input connector on board to support HT8000 in standalone mode. Alternately, power can be drawn from the HyperTransport connector.

The platform supports Xilinx Virtex II devices ranging from 4 million-gate to 8 million-gate FPGAs. The HT8000 is available with Embedded Linux.

Features

- Xilinx XC2V4000 to X2V8000 Virtex II FPGA series
- Two 16-bit HyperTransport Interfaces
- One 16-bit SPI-4 Interface
- Motorola MPC8245 processor
- 8 MB Flash
- 128 MB SDRAM
- Gigabit Ethernet Port
- Serial Port
- HyperTransport debug header
- 16-pin COP Header for CPU
- ATX power connector
- LED Indicators
- RTC
- Embedded Linux

Availability

The platform is now available in different Xilinx Virtex II configurations – XC2V4000, XC2V6000 and XC2V8000 FPGAs.

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