



AC97 Digital Controller

PRODUCT BRIEF

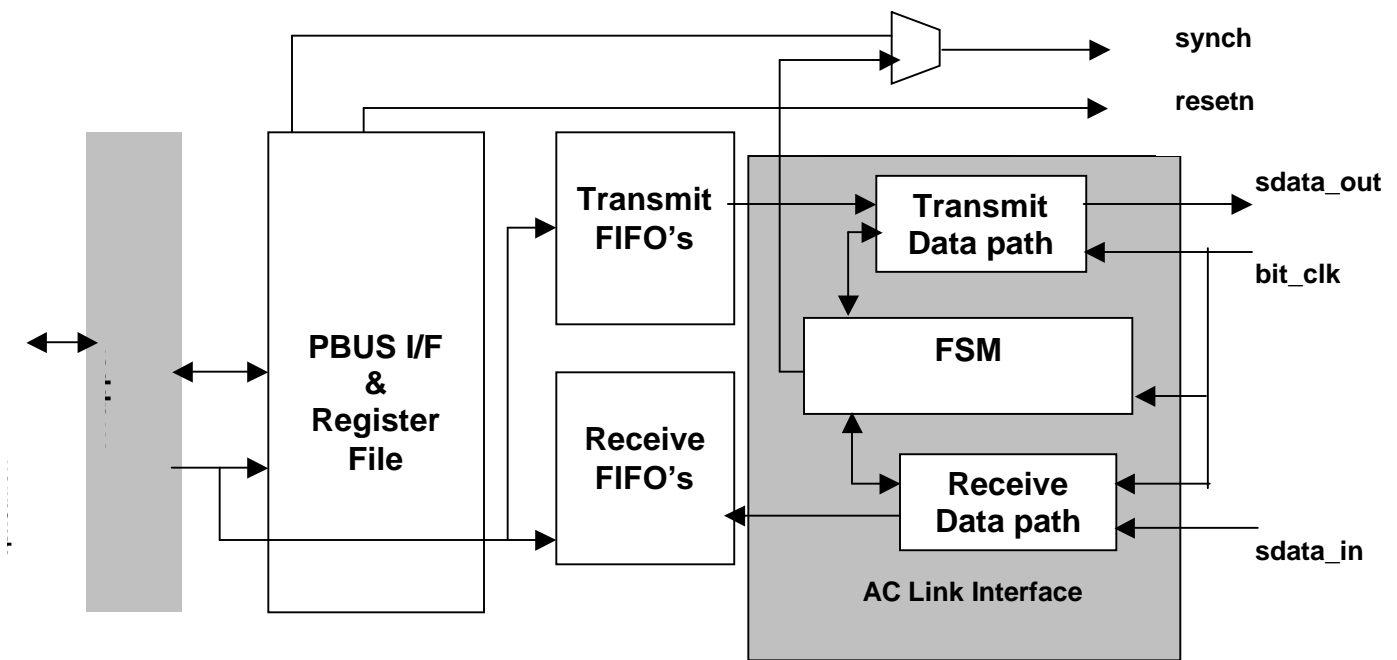
Overview Features

AC 97 is an audio controller equipped with AC link interface and an 8-bit peripheral bus interface. This core provides a high quality audio solution in combination with audio codec and driver software.

The controller is designed for easy integration into wide range of applications on most ASIC and FPGA technologies.

- ❑ AC97 rev 2.1 compliant
- ❑ Fully synchronous design
- ❑ Supports 8/16/18/20 bit ADC/DAC resolution
- ❑ Supports playback of mono audio sample stream
- ❑ Supports low power, sleep state and wake up modes
- ❑ Configurable FIFO depth and width
- ❑ PIO and DMA channels for FIFO data transfer
- ❑ Option to sample 1/2/4 samples per 32 bits
- ❑ 8 bit wide peripheral bus interface
- ❑ Loop back mode for test purpose

AC 97 Digital Controller





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Status: Gold [Silicon Proven]

Availability: Now

Language: Verilog

Synthesis: Synopsys DC

Simulation: Verilog XL or NC Verilog

Technology: 0.18 micron

Specifications

Configurable Options

- FIFO width and depth
- Input and output slots
- Number of FIFO's
- ADC/DAC Resolution

Design Attributes

- Fully synchronous
- Technology independent
- Loop back mode for testing
- PIO and DMA channels for FIFO data transfer
- Active low asynchronous reset
- Most of the logic functions at 12.2 Mhz
- 8 bit wide peripheral bus interface

Product Package

- Verilog RTL
- Verification environment
- Testcases
- Synthesis environment/scripts

Documentation

- Design
- User guide

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